

FIG. 1A PRIOR ART

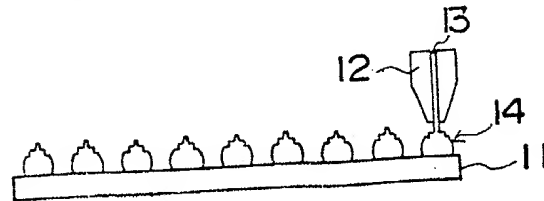


FIG. 1B PRIOR ART

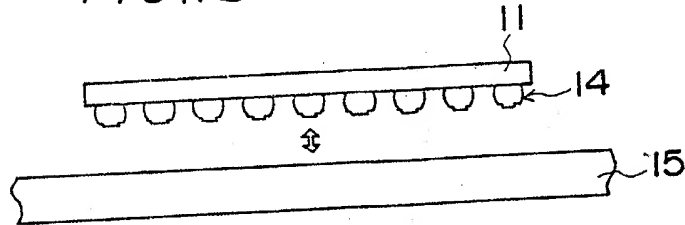


FIG. 1C PRIOR ART

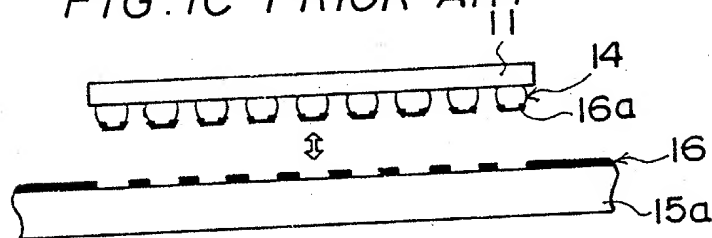


FIG. 1D PRIOR ART

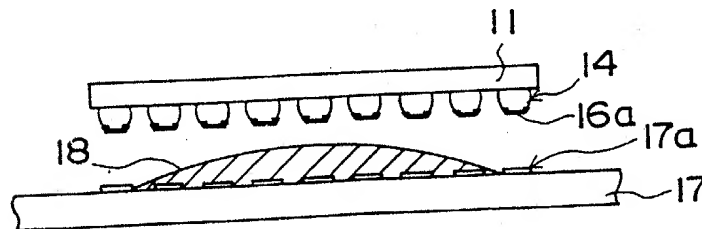


FIG. 1E PRIOR ART

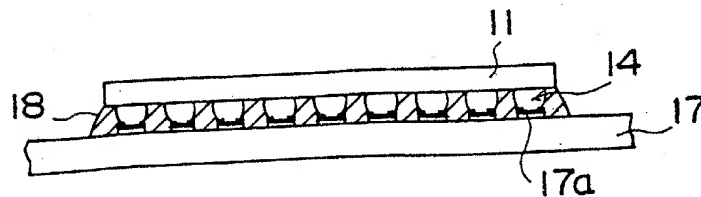


FIG. 2

21 FABRICATION SYSTEM

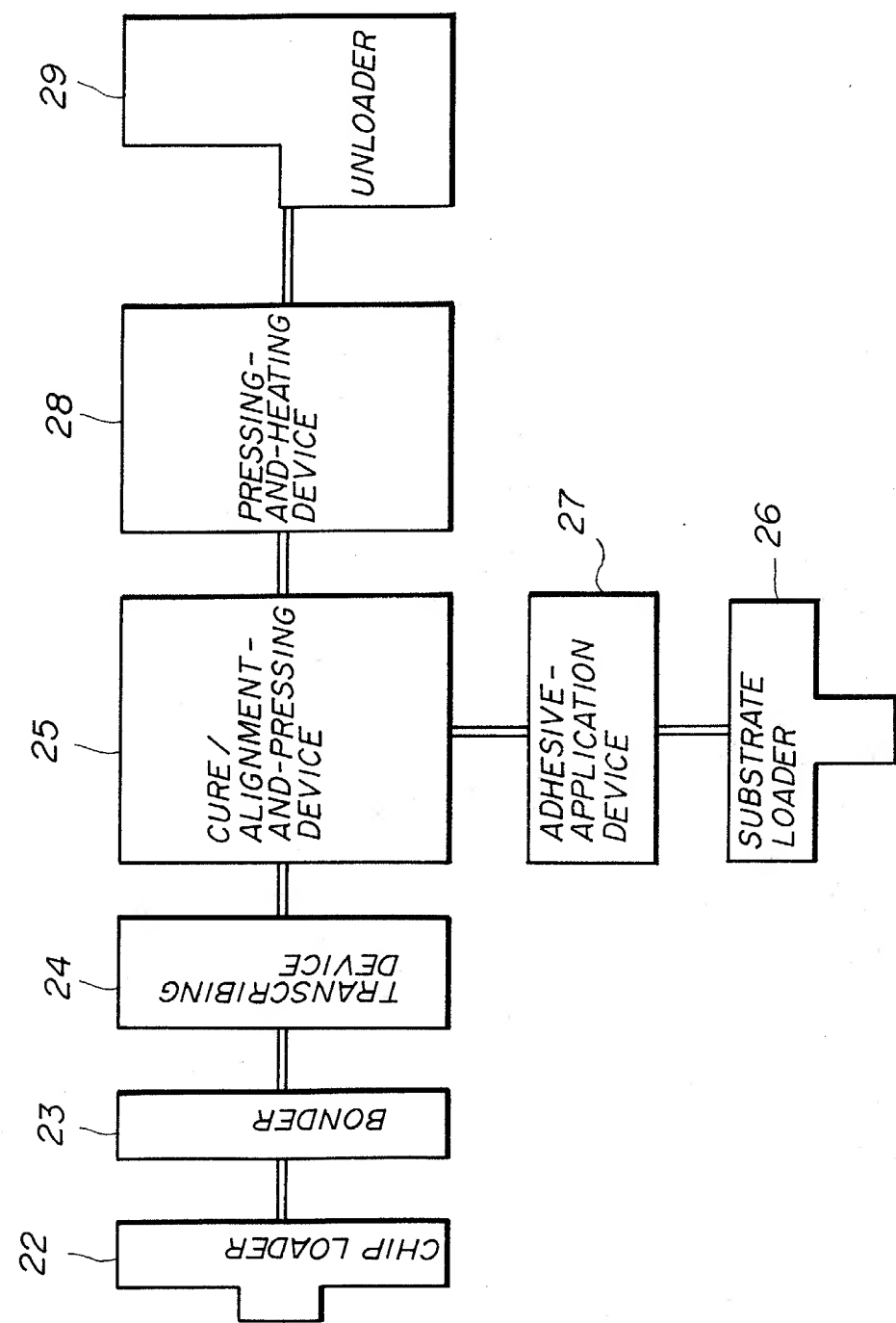
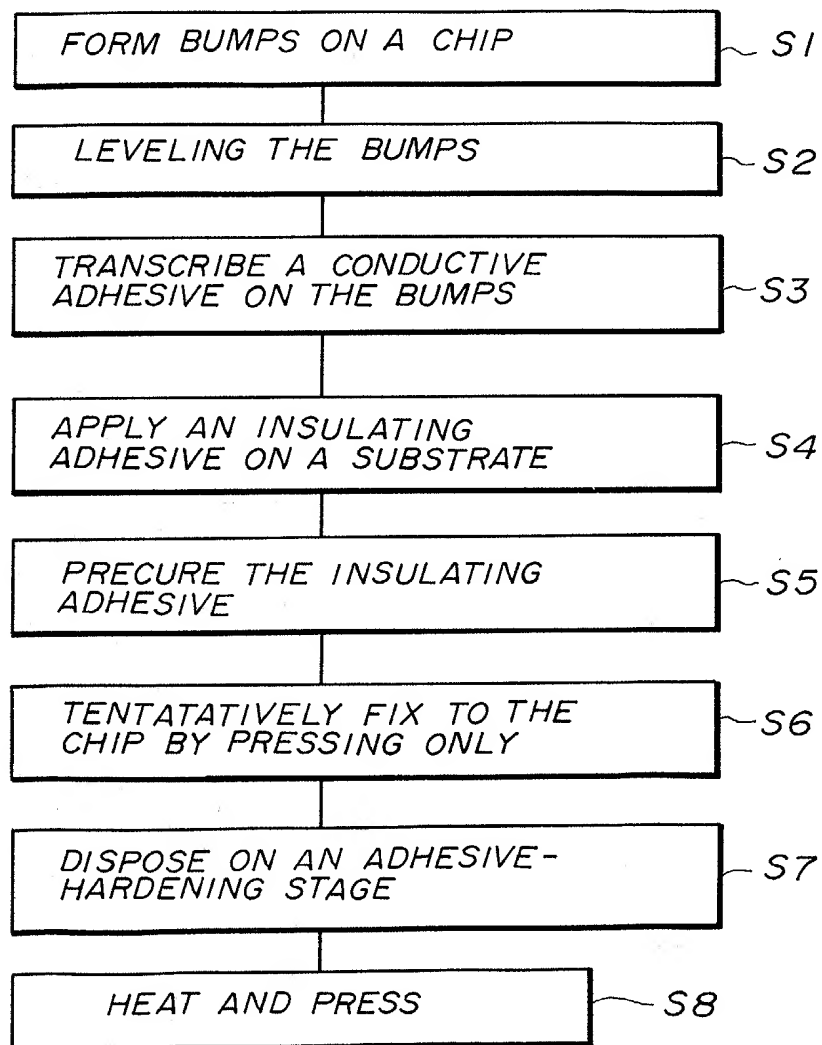
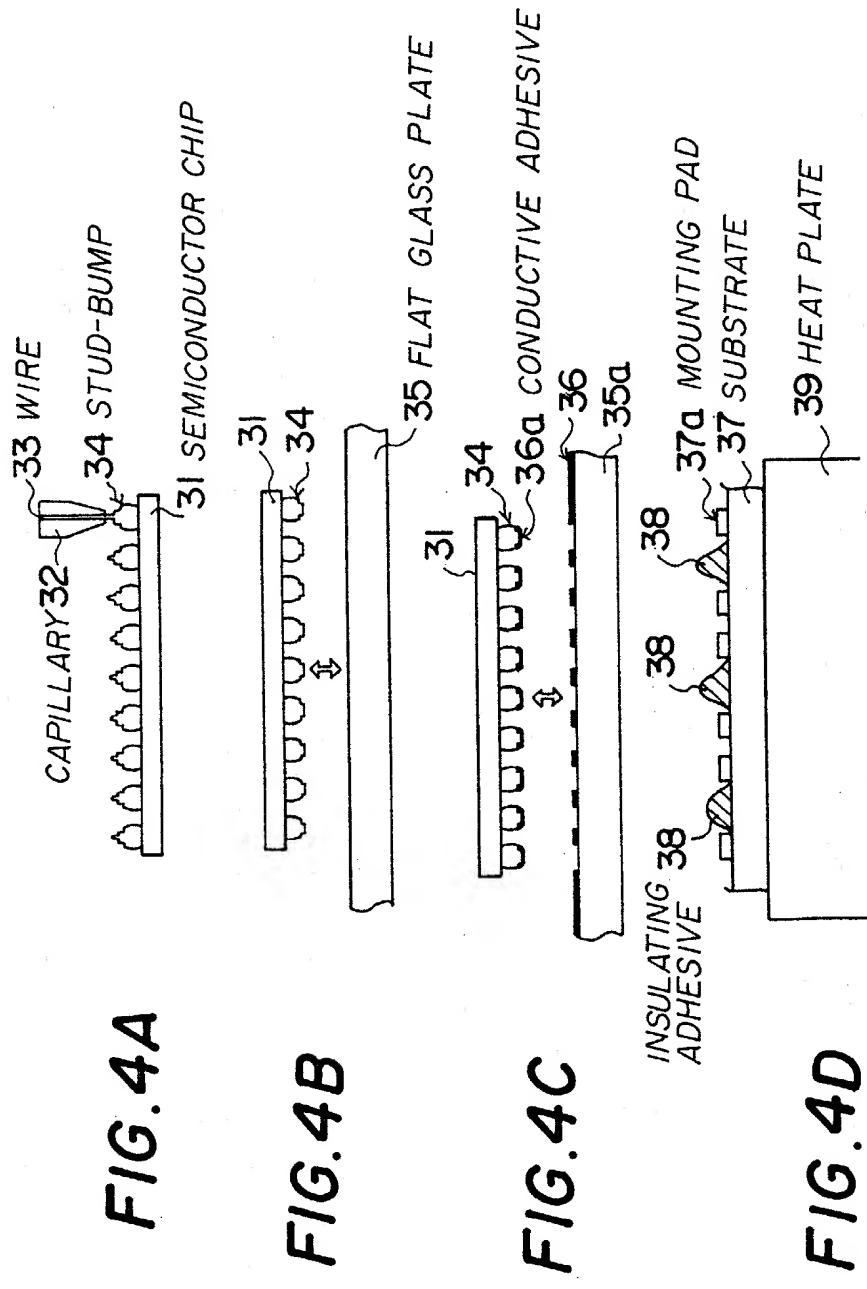


FIG. 3





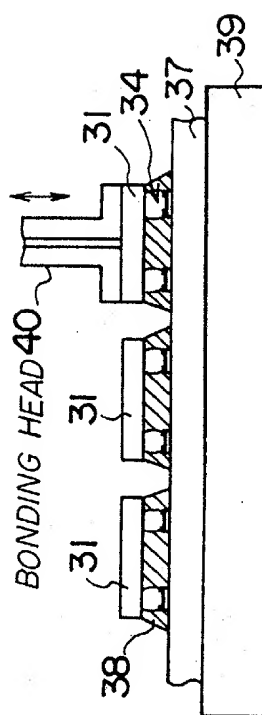


FIG. 4E

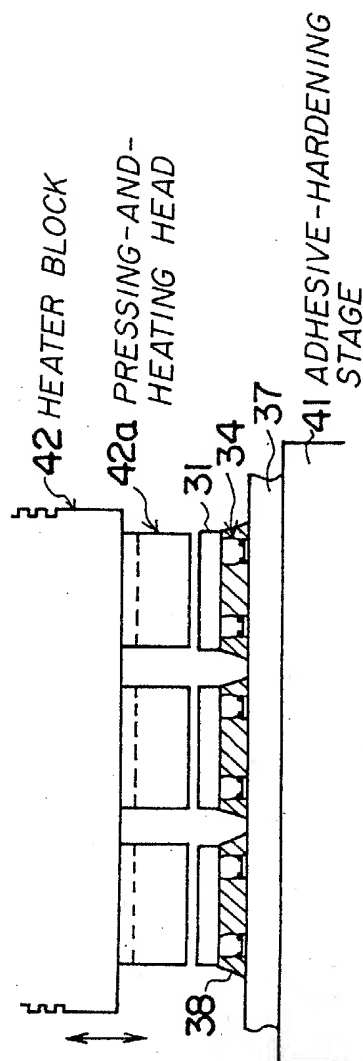


FIG. 4F

**FIG. 5**

51 SEMICONDUCTOR DEVICE

